## Identification of the limiting factors for high-temperature GaAs, GaInP, and AlGaInP solar cells from device and carrier lifetime analysis

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# Identification of the limiting factors for high-temperature GaAs, GaInP, and AIGaInP solar cells from device and carrier lifetime analysis

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We analyze the temperature-dependent dark saturation current density and open-circuit voltage  $(V_{OC})$  for GaAs, GaInP, and AlGaInP solar cells from 25 to 400 °C. As expected, the intrinsic carrier concentration,  $n_i$ , dominates the temperature dependence of the dark currents. However, at 400 °C, we measure  $V_{OC}$  that is ~50 mV higher for the GaAs solar cell and ~60–110 mV lower for the GaInP and AlGaInP solar cells compared to what would be expected from commonly used solar cell models that consider only the  $n_i^2$  temperature dependence. To better understand these deviations, we measure the carrier lifetimes of p-type GaAs, GaInP, and AlGaInP double heterostructures (DHs) from 25 to 400 °C using time-resolved photoluminescence. Temperature-dependent minority carrier lifetimes are analyzed to determine the relative contributions of the radiative recombination, interface recombination, Shockley-Read-Hall recombination, and thermionic emission processes. We find that radiative recombination dominates for the GaAs DHs with the effective lifetime approximately doubling as the temperature is increased from  $25 \,^{\circ}$ C to  $400 \,^{\circ}$ C. In contrast, we find that thermionic emission dominates for the GaInP and AlGaInP DHs at elevated temperatures, leading to a 3-4× reduction in the effective lifetime and  $\sim 40\times$  increase in the surface recombination velocity as the temperature is increased from 25 °C to 400 °C. These observations suggest that optimization of the minority carrier confinement layers for the GaInP and AlGaInP solar cells could help to improve  $V_{OC}$  and solar cell efficiency at elevated temperatures. We demonstrate  $V_{OC}$  improvement at 200–400 °C in GaInP solar cells fabricated with modified AlGaInP window and back surface field layers. Published by AIP Publishing. https://doi.org/10.1063/1.5003631

## **I. INTRODUCTION**

III-V materials have played an integral role in the advancement of a number of semiconductor technologies.<sup>1,2</sup> Nearly all light emitting and laser diodes are based on III-V materials due to the ability to combine a wide range of high quality materials with varying band structures into a single device.<sup>3</sup> The world's most efficient solar cells consist of multiple III-V semiconductor junctions and have achieved efficiencies above 45%, more than  $1.5 \times$  higher than that of the best single junction configurations.<sup>4–6</sup> While the majority of these optoelectronic devices are designed to operate around room temperature, there are many applications that would benefit from the development of semiconductor devices that can operate at much higher temperatures. For example, developing solar cells that can maintain a relatively high efficiency at elevated temperatures could be an enabling technology for hybrid photovoltaic-thermal (PV-T) systems or near-sun space missions.<sup>7-11</sup> In addition to demonstrating outstanding reliability in harsh conditions, III-V materials have also exhibited very low voltage temperature coefficients.<sup>12–14</sup> These factors indicate that III-V materials could be the materials of choice for applications that require operation in a high-temperature environment. We recently demonstrated a dual-junction solar cell that maintained an efficiency above 15% at 400 °C with several pathways to improve the performance further.<sup>15-18</sup> In addition to the development of robust device components, such as a stable front metallization and cell encapsulation, a thorough understanding of the limiting factors at high-temperature is necessary for improving the device design.

## **II. EXPERIMENTAL**

All of the solar cells and double heterostructures (DHs) in this study were grown at atmospheric pressure by organometallic vapor-phase epitaxy (OMVPE) using a custom-built vertical reactor on zinc-doped (001) GaAs substrates miscut 6° toward (111)A. Figure 1 shows the layer structure of the GaAs, GaInP, and AlGaInP solar cells analyzed in Sec. III, and includes the nominal thickness and the room-temperature bandgap of the semiconductor layers. All materials are latticematched to GaAs. Standard photolithography, electron-beam evaporation, and wet chemical etching techniques were used to process the samples into devices with an area of approximately  $0.1\,{\rm cm}^2$ . Room-temperature Hall and capacitance-voltage measurements were taken to estimate the doping of the emitter  $(N_D \approx 10^{18}\,{\rm cm}^{-3})$  and base  $(N_A \approx 10^{17}\,{\rm cm}^{-3})$  layers. Additional details on the growth conditions and cell results can be found elsewhere.  $^{15,19}$ 

The DHs, analyzed later in Figs. 4 and 5, have a nominal thickness of 500 nm and have the same composition, growth conditions, and doping levels as the base layers of the solar cells shown in Fig. 1. The cladding layers of these structures have the same composition and are grown under the same conditions as the back surface field (BSF) layers of the initial

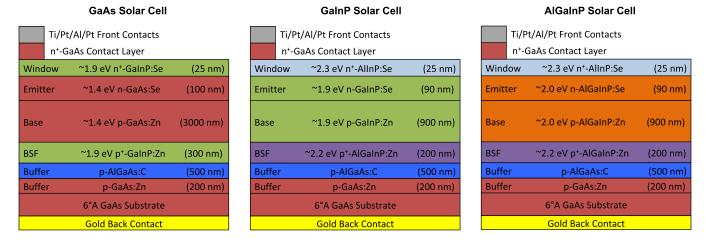


FIG. 1. Device schematic of the GaAs, GaInP, and AlGaInP solar cells showing the nominal room-temperature bandgaps, thicknesses, and the structure of the epitaxially grown layers. The diagram is not to scale.

set of solar cells. The nominal thickness of the GaInP DHs shown in Figs. 6 and 7 varies from 125 to 500 nm, but otherwise they are the same as the other GaInP DH.

To measure the solar cells and DHs at elevated temperatures, we use a temperature-controlled stage (Linkam HFS600E-PB4) capable of controlling the sample temperature from 25 to 600 °C. This stage is set up in conjunction with our existing current-voltage and flash testing setups to enable temperature-dependent measurements of the cells. Additional details are described elsewhere.<sup>15</sup> Charge carrier lifetimes were measured using time-resolved photoluminescence (TRPL).<sup>20</sup> Excitation was at 480 nm for the GaInP and AlGaInP DHs and 640 nm for the GaAs DHs with 300 fs pulses and a 1.1 MHz laser repetition rate. The excitation spot diameter was  $\approx 0.3$  mm, and the average excitation power was 0.4 mW. TRPL decays were measured at the maxima of the temperature-dependent emission bands by using appropriate bandpass interference filters. Timecorrelated single photon counting with a silicon avalanche photodiode provided  $\approx 0.02$  ns time resolution, and deconvolution was not necessary in the data analysis. Exponential fits to TRPL data were used to determine minority carrier lifetimes measured in low-level injection conditions. A multimode optical fiber was used to deliver excitation pulses and to collect TRPL signals.<sup>20</sup>

## **III. DEVICE ANALYSIS**

One approach to understand the device limitations is based on a dark current analysis of single-junction solar cells. The current-voltage (JV) characteristics of III-V solar cells frequently follow a two-diode model<sup>21,22</sup>

$$J = J_{01} \left( e^{q(V - JR_S)/kT} - 1 \right) + J_{0n} \left( e^{q(V - JR_S)/nkT} - 1 \right) + \frac{V - JR_S}{R_{Shunt}} - J_L,$$
(1)

where J is the current density,  $J_{01}$  is the dark saturation current density arising from recombination in the quasi-neutral regions,  $J_{0n}$  is the dark saturation current density arising from recombination in the depletion region and at the cell perimeter, n is the ideality factor associated with  $J_{0n}$ , q is the elementary charge, V is the applied voltage, k is the Boltzmann constant, T is the temperature,  $R_S$  is the series resistance,  $R_{Shunt}$  is the shunt resistance, and  $J_L$  is the photocurrent density. At high current densities, III-V solar cells are typically dominated by recombination in the quasineutral regions, a result of the larger exponent in the  $J_{01}$  term, and the  $J_{0n}$  factor will have a minimal impact on the cell voltage. At these high current densities, the open-circuit voltage ( $V_{OC}$ ) can be approximated from the following equation:

$$V_{OC} = \frac{kT}{q} ln \left( \frac{J_L}{J_{01}} \right).$$
<sup>(2)</sup>

The analytical drift-diffusion model separates the  $J_{0I}$  dark saturation current densities of a p-n junction into components corresponding to recombination in the n-type and p-type regions<sup>21–23</sup>

$$J_{01,nregion} = q \frac{n_i^2}{N_D} \sqrt{\frac{D_p}{\tau_p}} \left[ \frac{\frac{S_p L_p}{D_p} \left( \cosh\left(\frac{x_n}{L_p}\right) \right)^2 + \sin h\left(\frac{x_n}{L_p}\right)}{\frac{S_p L_p}{D_p} \sin h\left(\frac{x_n}{L_p}\right) + \cos h\left(\frac{x_n}{L_p}\right)} \right],$$
(3)

$$J_{01,pregion} = q \frac{{n_i}^2}{N_A} \sqrt{\frac{D_n}{\tau_n}} \left[ \frac{\frac{S_n L_n}{D_n} \left( \cos h\left(\frac{x_p}{L_n}\right) \right)^2 + \sin h\left(\frac{x_p}{L_n}\right)}{\frac{S_n L_n}{D_n} \sin h\left(\frac{x_p}{L_n}\right) + \cos h\left(\frac{x_p}{L_n}\right)} \right], \tag{4}$$

where  $n_i$  is the intrinsic carrier concentration,  $N_D$  is the donor concentration in the n-type region,  $N_A$  is the acceptor concentration in the p-type region,  $D_p$  is the minority carrier diffusion coefficient for holes,  $D_n$  is the minority carrier diffusion coefficient for electrons,  $\tau_p$  is the minority carrier lifetime for holes,  $\tau_n$  is the minority carrier lifetime for electrons,  $S_p$  is the surface recombination velocity for minority holes,  $S_n$  is the surface recombination velocity for minority electrons,  $L_p$  is the minority carrier diffusion length for

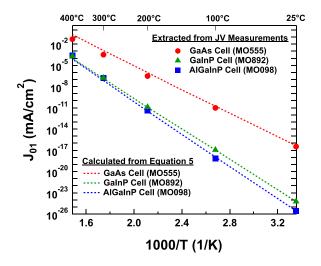


FIG. 2. Comparison between measured and modeled  $J_{01}$  dark saturation current densities for GaAs, GaInP, and AlGaInP solar cells. The markers show the  $J_{01}$  dark saturation current densities extracted from temperaturedependent JV measurements, and the dashed lines show the  $J_{01}$  dark saturation current densities calculated from Eq. (5). The calculated  $J_{01}$  dark saturation current densities are adjusted for agreement at 25 °C.

holes,  $L_n$  is the minority carrier diffusion length for electrons,  $x_n$  is the thickness of the n-type region, and  $x_p$  is the thickness of the p-type region. By far, the most important impact of increasing the temperature is the exponential increase in the intrinsic carrier concentration,  $n_i$ , which in turn leads to an exponential rise in the  $J_{01}$  dark saturation current densities and consequently a drop in the  $V_{OC}$  of a solar cell. The temperature dependence of the  $n_i^2$  factor in Eqs. (3) and (4) can be described by the following proportionality, where  $E_g$  is the bandgap:

$$n_i^2 \alpha T^3 e^{-E_g(T)/kT}.$$
 (5)

To test the validity range of this model, we grew singlejunction GaAs ( $E_{g,25^{\circ}C} \approx 1.4 \text{ eV}$ ), GaInP ( $E_{g,25^{\circ}C} \approx 1.9 \text{ eV}$ ), and AlGaInP ( $E_{g,25^{\circ}C} \approx 2.0 \text{ eV}$ ) solar cells, as shown in Fig. 1.

We extract  $J_{01}$  for the GaInP and AlGaInP cells by fitting dark JV measurements to Eq. (1) and observe a clear n=1 region above ~10 mA/cm<sup>2</sup> for all temperatures between 25 and 400 °C. For the GaAs solar cell, we are unable to fit dark JV measurements to the n=1 line at the highest temperatures due to series resistance effects and instead extract  $J_{01}$  from concentrator measurements. Additional details are reported elsewhere.<sup>15,19</sup> The markers in Fig. 2 show the temperature-dependent  $J_{01}$  dark saturation current densities extracted from JV measurements, while the dashed lines show the  $J_{01}$  dark saturation current densities calculated from the proportionality of Eq. (5), adjusted for agreement at room temperature.

It is clear from these plots that the  $n_i^2$  factor dominates the  $J_{01}$  temperature dependence, driving a ~15–22 order-ofmagnitude increase in  $J_{01}$  for the three cells as the temperature is increased from 25 °C to 400 °C. Since  $n_i$  is the dominant factor, solar cell models often make the simplifying assumption that the other factors in Eqs. (3) and (4) are constant with temperature.<sup>16,24–26</sup> At the highest temperatures, however, the data deviate slightly from the model, as can be seen on the left in Fig. 2. This deviation is clearer when considering  $V_{OC}$ .

In Fig. 3, the solid markers show  $V_{OC}$  at a photocurrent density of 5 A/cm<sup>2</sup> (~500 suns) for the GaAs, GaInP, and AlGaInP solar cells as measured using a High Intensity Pulsed Solar Simulator (HIPSS).<sup>27</sup> The solid lines represent the expected temperature-dependence of  $V_{OC}$  from Eqs. (2) and (5), where the  $J_{01}$  dark saturation current densities are again adjusted for agreement at 25 °C. For the GaInP cell, we also include data showing  $V_{OC}$  calculated from Eq. (2) but using the  $J_{01}$  dark saturation current densities that were extracted from dark JV measurements, and the results from this technique are in good agreement with the HIPSS data. Based on repeated measurements of single-junction solar cells at the same light intensity, we observe a variation of less than 10 mV in  $V_{OC}$  on the HIPSS.

Equations (2) and (5) give a reasonable approximation of  $V_{OC}$  at elevated temperatures, but we still measure >50 mV deviation in  $V_{OC}$  from these analytical models. For the GaAs cell, the measured  $V_{OC}$  is higher than the model, while for the GaInP and AlGaInP cells, the measured  $V_{OC}$  is lower. To understand these deviations, we reconsider the validity of the underlying assumption that only the temperature dependence of  $n_i$  is significant.

#### **IV. CARRIER LIFETIME ANALYSIS**

To investigate the temperature dependence of the other factors in Eqs. (3) and (4), we study the carrier lifetimes of

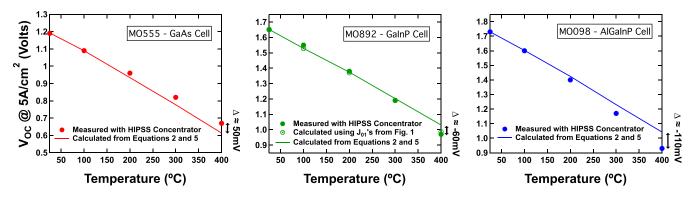


FIG. 3. Temperature dependence of  $V_{OC}$  at a photocurrent of 5 A/cm<sup>2</sup> (~500 suns) for GaAs, GaInP, and AlGaInP solar cells are measured using the HIPSS (solid markers) and are calculated from Eqs. (2) and (5) (lines). For the GaInP cell, we also include data showing  $V_{OC}$  calculated from Eq. (2) but using the  $J_{\partial I}$  dark saturation current densities that were extracted from dark JV measurements (open markers).

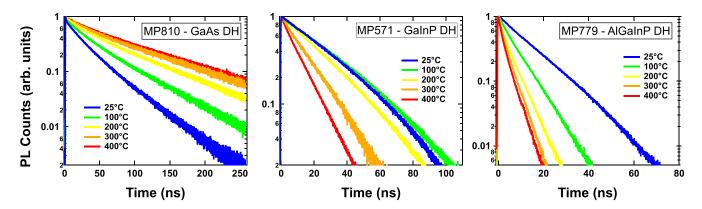


FIG. 4. Normalized TRPL decay curves for GaAs, GaInP, and AlGaInP DHs measured at 25–400 °C. At elevated temperatures, we observe longer effective TRPL lifetimes for the GaAs DH and shorter effective TRPL lifetimes for the GaInP and AlGaInP DHs.

p-type GaAs, GaInP, and AlGaInP DHs. Figure 4 shows the normalized decay curves for the GaAs, GaInP, and AlGaInP DHs at 25-400 °C.

We observe that the TRPL decays of the GaAs DH are slower at elevated temperatures, but we see the opposite trend for the GaInP and AlGaInP DHs.<sup>28–30</sup> This is consistent with the temperature dependences of  $V_{OC}$  in Fig. 3. In other words, a GaAs solar cell with an effective lifetime that increases at higher temperature would have a lower  $J_{01}$  and higher  $V_{OC}$  than we would expect from Eqs. (2) and (5). Similarly, a GaInP or an AlGaInP solar cell with a lifetime that decreases at higher temperature would have a higher  $J_{01}$ and lower  $V_{OC}$  than we would expect from Eqs. (2) and (5).

Temperature-dependent lifetimes, extracted from the decay curves of Fig. 4, are fit to a recombination model to determine the relative contributions of the radiative recombination (lifetime  $\tau_{rad}$ ), Shockley-Read-Hall recombination (lifetime  $\tau_{sRH}$ ), interface recombination (lifetime  $\tau_{int}$ ), and thermionic emission (lifetime  $\tau_{th}$ ) processes. Here,  $\tau_{int}$  refers to the recombination of carriers due to traps at the interface between the absorber and barrier layers, whereas  $\tau_{th}$  refers to the thermal escape of carriers over the barrier. The Shockley-Read Hall and interface lifetimes have the same temperature dependence and are thus considered together. The analytical descriptions for the temperature dependences of  $\tau_{rad}$ ,  $\tau_{SRH}$ ,  $\tau_{int}$ , and  $\tau_{th}$  are described in the literature.<sup>31,32</sup>

For the analysis of our data, the temperature dependences of these lifetimes can be simplified  $as^{33}$ 

$$\tau_{rad} \, \alpha \, C_{rad} \mathrm{T}^{1.5}, \tag{6}$$

$$\tau_{SRH,int} \, \alpha \, C_{SRH,int} \mathrm{T}^{-0.5}, \tag{7}$$

$$\tau_{th} \,\alpha \, C_{th} \mathrm{T}^{-0.5} * e^{\Delta \mathrm{E}/kT},\tag{8}$$

where  $\Delta E$  is the effective barrier height and  $C_{rad}$ ,  $C_{SRH,int}$ , and  $C_{th}$  are temperature-independent proportionality constants for the radiative recombination, Shockley-Read-Hall recombination,<sup>32</sup> interface recombination, and thermionic emission processes, respectively.<sup>31,33</sup> The effective recombination rate,  $1/\tau_{eff}$ , includes all recombination pathways

$$\frac{1}{\tau_{eff}} = \frac{1}{\tau_{rad}} + \frac{1}{\tau_{SRH}} + \frac{1}{\tau_{int}} + \frac{1}{\tau_{th}}.$$
 (9)

The markers in Fig. 5 show the effective carrier lifetimes measured by TRPL as a function of temperature for the GaAs, GaInP, and AlGaInP DHs. The dashed lines in Fig. 5 indicate fitting results of the data to Eqs. (6)–(9). The GaAs DH is dominated by radiative recombination, with only a small contribution from the other terms at the highest temperatures. This drives a  $\sim 2.5 \times$  increase in the effective lifetime as the temperature is increased from 25 °C to 400 °C. While the

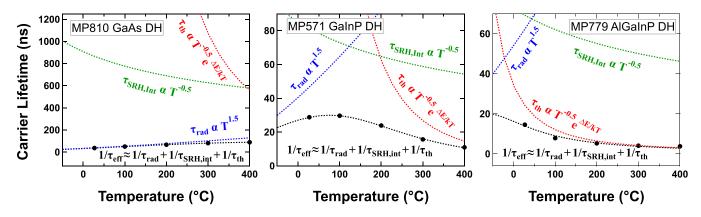


FIG. 5. Measured carrier lifetime temperature dependence for GaAs, GaInP, and AlGaInP DHs (solid markers). The dashed lines indicate fitting results for  $\tau_{eff}$  (black),  $\tau_{rad}$  (blue),  $\tau_{SRH,int}$  (green), and  $\tau_{th}$  (red). Note the difference in the vertical scales between the three graphs.

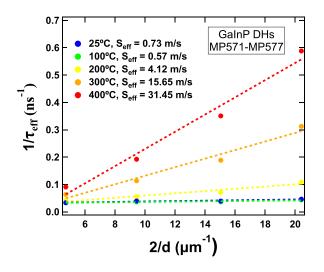


FIG. 6. Plots of the inverse effective lifetime versus inverse sample thickness, 2/d, for GaInP DHs.  $S_{eff}(T)$  is extracted from the slope of each curve.

radiative lifetime is dominant for the GaInP DH at 25 °C, the other recombination mechanisms quickly overtake it as the temperature is increased, and thermionic emission becomes dominant above ~200 °C. This drives a ~3× decrease in the effective lifetime at 400 °C. For the AlGaInP DH, thermionic emission is dominant at all temperatures, and this drives a ~4× decrease in the effective lifetime as the temperature is increased from 25 °C to 400 °C.

Next, we consider the surface recombination. Assuming that the interface recombination and thermionic emission processes can be parameterized by recombination velocities, the effective surface recombination velocity ( $S_{eff}$ ) can be taken as the sum of the interface recombination velocity ( $S_{int}$ ) and thermionic emission recombination velocity ( $S_{th}$ ), and Eq. (9) for the effective lifetime  $\tau_{eff}$  can be re-expressed in terms of  $S_{eff}^{34}$ 

$$\frac{1}{\tau_{eff}} = \frac{1}{\tau_{rad}} + \frac{1}{\tau_{SRH}} + \frac{2S_{eff}}{d},$$
(10)

where *d* is the thickness of the DH.<sup>33,35</sup> This analysis is most important for the GaInP DH, where we observe the transition from the radiative recombination to the Shockley-Read-Hall recombination to the thermionic emission [Fig. 5(b)]. To determine the temperature dependence of  $S_{eff}$ , we grew four GaInP DHs of varying thickness (nominally 125–500 nm). More accurate thicknesses for the DHs were determined from the interference fringes of a 632 nm laser reflecting off the epilayer surface during growth. Effective lifetimes were determined for these DHs at 25–400 °C using TRPL. Plots of  $1/\tau_{eff}$  versus 2/d are shown in Fig. 6, with linear fits to the data shown as dashed lines.  $S_{eff}$  can be determined from the slope of these curves, as is evident from Eq. (10).<sup>29</sup>

We observe a  $\sim 40 \times$  increase in  $S_{eff}$  as the temperature is increased from 25 °C to 400 °C. The relative contribution from  $S_{int}$  and  $S_{th}$  can be deconvolved by fitting these data to Eqs. (7)–(10),

$$S_{eff} = \frac{d}{2\tau_{int}} + \frac{d}{2\tau_{th}} = C_1 T^{0.5} + C_2 T^{0.5} e^{-\Delta E/kT}.$$
 (11)

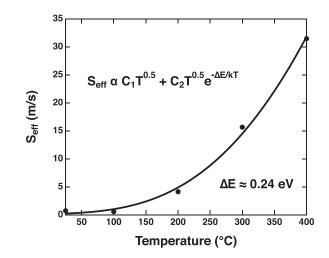


FIG. 7. Plots of  $S_{eff}(T)$  for GaInP double heterostructures (solid markers). A fit of this data to Eq. (11) is also shown (solid line), allowing us to extract a value of 0.24 eV for  $\Delta E$ .

Here,  $C_1$  and  $C_2$  are temperature independent proportionality constants. Figure 7 shows a fit of  $S_{eff}(T)$  to Eq. (11), where the values for  $S_{eff}$  are taken from Fig. 6.

The fits are entirely dominated by the exponential function in the thermionic emission term, and we are able to extract a barrier height for minority electrons at the GaInP/ AlGaInP interface,  $\Delta E = 0.24 \pm 0.01 \text{ eV}$ , very close to what we expect for these two materials.

#### **V. DISCUSSION**

Given the uncertainties in the modeling and measurement, and the fact that the solar cells have both n-side and pside effects whereas the DH experiments were only on ptype samples, it is not possible to directly apply the measured DH lifetimes to Eqs. (3) and (4). The carrier lifetime analysis will, however, allow us to approximate the impact of the temperature-dependent lifetimes on Voc. For the GaAs DH,  $\tau_{rad}$  dominates the minority carrier lifetime across the entire temperature range and drives a  $\sim 2.5 \times$  increase in the bulk lifetime as the temperature is increased to 400 °C. Since the bulk lifetime shows up in the denominator of Eqs. (3) and (4), we would expect  $\tau_{rad}$  to drive a ~1.5× decrease in  $J_{01}$ relative to Eq. (5), and this will lead to a  $\sim$ 30 mV increase in  $V_{OC}$  at 400 °C for a GaAs solar cell compared to Eqs. (2) and (5). The increasing bulk lifetime makes up a significant fraction of the deviation that we observe in Fig. 3. Note that our carrier lifetime analysis was for p-type DHs, but we expect similar conclusions for n-type DHs based on the bandgaps and band offsets.

Next, we analyze the potential to increase  $V_{OC}$  of GaInP solar cells based on our findings that at elevated temperatures, the effective lifetime is dominated by thermionic emission. An analysis of the impact of  $S_{eff}$  on  $V_{OC}$  of a solar cell is reported in Ref. 23, and it is shown that the  $\sim 40 \times$  increase in  $S_{eff}$  that we observe as the temperature is increased from 25 °C to 400 °C can reduce  $V_{OC}$  by  $\sim 100$  mV in the expected operating regime of our solar cells where the thickness exceeds the diffusion length and  $0.01 < \frac{SL}{D} < 100$ .

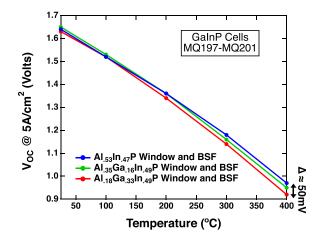


FIG. 8. Temperature dependence of  $V_{OC}$  at a photocurrent of  $5 \text{ A/cm}^2$  (~500 suns) for GaInP solar cells with varying window layers and BSF compositions.

We are able to explore the significance of the passivation layers on  $V_{OC}(T)$  experimentally by growing GaInP solar cells with different minority carrier confinement layers. For this study, we grew three GaInP solar cells with varying aluminum compositions for the AlGaInP window and back surface field (BSF) layers. The nominal aluminum compositions of these layers are 18% (Al<sub>0.18</sub>Ga<sub>0.33</sub>In<sub>0.49</sub>P, E<sub>g,25°C</sub>  $\approx$  2.1 eV), 35% (Al<sub>0.35</sub>Ga<sub>0.16</sub>In<sub>0.49</sub>P, E<sub>g,25°C</sub>  $\approx$  2.2 eV), and 53% (Al<sub>0.53</sub>In<sub>0.47</sub>P, E<sub>g,25°C</sub>  $\approx$  2.3 eV) based on calibrations of the growth reactor. Figure 8 shows the temperature dependence of  $V_{OC}$  at a photocurrent density of 5 A/cm<sup>2</sup> for these three samples as measured using the HIPSS.

While we measure a similar  $V_{OC}$  for each sample at room temperature, we begin to see the expected divergence in  $V_{OC}$  as thermionic emission overtakes the other recombination mechanisms. At 400 °C,  $V_{OC}$  increases by ~50 mV when the bandgap of the minority carrier confinement layers is increased by  $\sim 200 \text{ meV}$ . These results indicate that additional efforts to improve the design of the minority carrier confinement layers could notably improve the performance of a GaInP or AlGaInP solar cell at elevated temperatures. It is important to note, however, that there is a significant challenge in further reducing the effect of thermionic emission for a GaInP or AlGaInP solar cell because material constraints limit the bandgap of a lattice-matched III-V cladding layer to less than  $\sim 2.3 \text{ eV}$  (Al<sub>0.53</sub>In<sub>0.47</sub>P). For GaAs solar cells, however, these materials can be used as effective barriers, and the data presented in Figs. 3 and 5 indicate that the barrier remains effective even at temperatures as high as 400 °C.

## **VI. CONCLUSIONS**

In summary, we analyze the  $J_{01}$  dark saturation current densities and  $V_{OC}$  of GaAs, GaInP, and AlGaInP solar cells from 25 to 400 °C. While the intrinsic carrier concentration dominates the temperature dependence of  $J_{01}$ , we measure  $V_{OC}$  for the GaAs solar cell that is higher and  $V_{OC}$  for the GaInP and AlGaInP solar cells that are lower than we would expect from commonly used solar cell models that consider only the  $n_i^2$  temperature dependence. To better understand these deviations, we analyze the carrier lifetime of GaAs, GaInP, and AlGaInP DHs from 25 to 400 °C using TRPL. We find that the performance of the GaAs DH is dominated by radiative recombination at all temperatures and this drives an increase in the effective lifetime at high temperatures. On the other hand, carrier lifetimes in the GaInP and AlGaInP DHs are dominated by thermionic emission and this leads to a  $3-4\times$  reduction in the carrier lifetime as the temperature is increased from 25 °C to 400 °C. In this temperature range, the surface recombination velocity for the GaInP correspondingly increases by  $\sim 40 \times$ . The lower effective lifetime and higher surface recombination velocity can explain why  $V_{OC}$ of the GaInP and AlGaInP solar cells are lower than predicted by the  $n_i^2$  temperature dependence of  $J_{01}$ . These studies indicate that additional efforts to improve the design of the minority carrier confinement layers for GaInP and AlGaInP solar cells could help to improve  $V_{OC}$  and efficiency at elevated temperatures, although material constraints place a practical limitation on the achievable barrier height for III-V materials lattice-matched to GaAs.

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